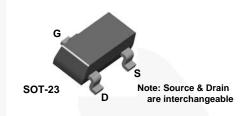


# MMBFJ309 / MMBFJ310 N-Channel RF Amplifier

# Description

This device is designed for VHF/UHF amplifier, oscillator and mixer applications. As a common gate amplifier, 16 dB at 100 MHz and 12 dB at 450 MHz can be realized. Sourced from process 92. Source & Drain are interchangeable.



# **Ordering Information**

Part Number	Top Mark	Package	Packing Method
MMBFJ309	6U	SOT-23 3L	Tape and Reel
MMBFJ310	6T	SOT-23 3L	Tape and Reel

# Absolute Maximum Ratings<sup>(1), (2)</sup>

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_A = 25^{\circ}$ C unless otherwise noted.

Symbol	Parameter	Value	Unit
V <sub>DG</sub>	Drain-Gate Voltage	25	V
V <sub>GS</sub>	Gate-Source Voltage	-25	V
I <sub>GF</sub>	Forward Gate Current	10	mA
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Junction Temperature Range	-55 to 150	°C

### Notes:

- 1. These ratings are based on a maximum junction temperature of 150°C.
- 2. These are steady-state limits. Fairchild Semiconductor should be consulted on applications involving pulsed or low-duty-cycle operations.

### Thermal Characteristics<sup>(3)</sup>

Values are at  $T_A = 25^{\circ}C$  unless otherwise noted.

Symbol	Parameter	Max.	Unit
р	Total Device Dissipation	350	mW
PD	Derate Above 25°C	2.8	mW/°C
R <sub>θJA</sub>	Thermal Resistance, Junction-to-Ambient	357	°C/W

### Note:

3. Device mounted on FR-4 PCB 36mm × 18mm × 1.5mm; mounting pad for the collector lead minimum 6cm<sup>2</sup>.

January 2015

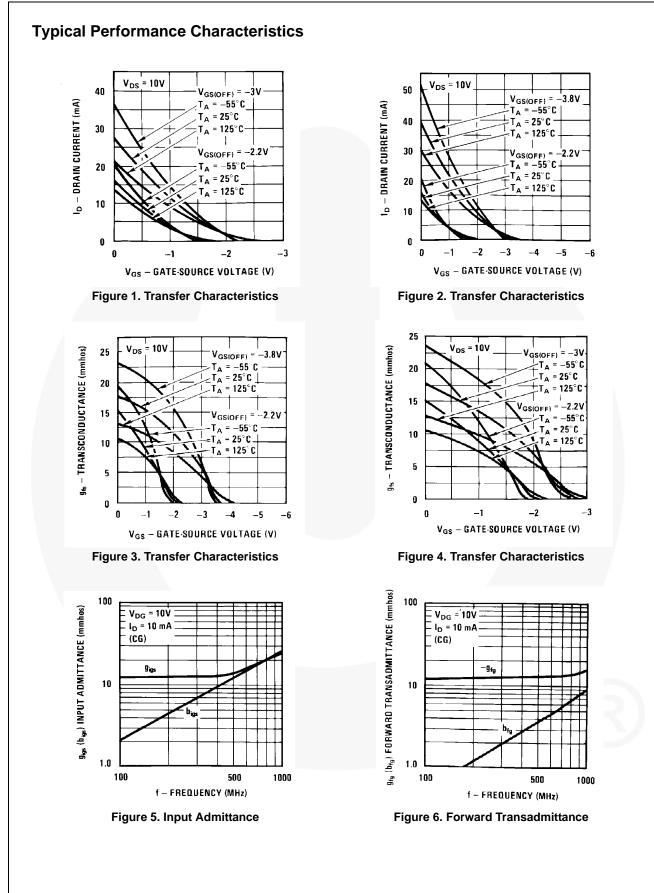
# **Electrical Characteristics**

Values are at  $T_A = 25^{\circ}C$  unless otherwise noted.

Symbol	Parameter	Conditions	6	Min.	Тур.	Max.	Unit
Off Chara	acteristics						
V <sub>(BR)GSS</sub>	Gate-Source Breakdown Voltage	$I_{G} = -1.0 \ \mu A, \ V_{DS} = 0$		-25			V
1	Cata Bayaraa Currant	V <sub>GS</sub> = -15 V, V <sub>DS</sub> = 0				-1.0	nA
I <sub>GSS</sub>	Gate Reverse Current	$V_{GS} = -15 \text{ V}, V_{DS} = 0, T_A = 125^{\circ}\text{C}$				-1.0	μA
V	Gate-Source Cut-Off Voltage	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1.0 nA	MMBFJ309	-1.0		-4.0	V
V <sub>GS(off)</sub>	Gale-Source Cut-Oil Vollage		MMBFJ310	-2.0		-6.5	
On Chara	cteristics				-	-	
I	Zero-Gate Voltage Drain Current <sup>(4)</sup>	$V_{DS} = 10 \text{ V}, \text{ V}_{GS} = 0$	MMBFJ309	12		30	mA
IDSS			MMBFJ310	24		60	
V <sub>GS(f)</sub>	Gate-Source Forward Voltage	$V_{DS} = 0, I_{G} = 1.0 \text{ mA}$				1.0	V
Small Sig	nal Characteristics		_				
Re <sub>(yis)</sub>	Common-Source Input	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 10 \text{ mA},$	MMBFJ309		0.7		mmhos
(yis)	Conductance	f = 100 MHz	MMBFJ310		0.5		
Re <sub>(yos)</sub>	Common-Source Output Conductance	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 10 \text{ mA}, \text{ f} = 100 \text{ MHz}$			0.25		mmhos
G <sub>pg</sub>	Common-Gate Power Gain	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 10 \text{ mA}, \text{ I}_{D}$	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 10 mA, f = 100 MHz		16		dB
Re <sub>(yfs)</sub>	Common-Source Forward Transconductance	$V_{DS} = 10 \text{ V}, I_D = 10 \text{ mA}, 10 \text{ mA}$	f = 100 MHz		12		mmhos
Re <sub>(yig)</sub>	Common-Gate Input Conductance	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 10 \text{ mA}, \text{ I}_{D}$	f = 100 MHz		12		mmhos
~	Common-Source Forward	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 10 mA,	MMBFJ309	10000		20000	μmhos
9 <sub>fs</sub>	Transconductance	f = 1.0 kHz	MMBFJ310	8000		18000	
g <sub>oss</sub>	Common-Source Output Conductance	$V_{DS} = 10 \text{ V}, I_{D} = 10 \text{ mA}, 10 \text{ mA}$	f = 1.0 kHz			150	μmhos
a	Common-Gate Forward Conductance	$V_{DS} = 10 \text{ V}, I_{D} = 10 \text{ mA},$ f = 1.0 kHz	MMBFJ309		13000		μmhos
9 <sub>fg</sub>			MMBFJ310		12000		
a.			MMBFJ309		100		μmhos
9 <sub>og</sub>			MMBFJ310		150		
C <sub>dg</sub>	Drain-Gate Capacitance	V <sub>DS</sub> = 0, V <sub>GS</sub> = -10 V, f = 1.0 MHz			2.0	2.5	pF
C <sub>sg</sub>	Source-Gate Capacitance	$V_{DS} = 0, V_{GS} = -10 V, f = 1.0 MHz$			4.1	5.0	pF
NF	Noise Figure	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 10 \text{ mA}, \text{ f} = 450 \text{ MHz}$			3.0		dB
e <sub>n</sub>	Equivalent Short-Circuit Input Noise Voltage	$V_{DS} = 10 \text{ V}, I_{D} = 10 \text{ mA}, 10 \text{ mA}$	f = 100 Hz		6.0		nV.//Hz

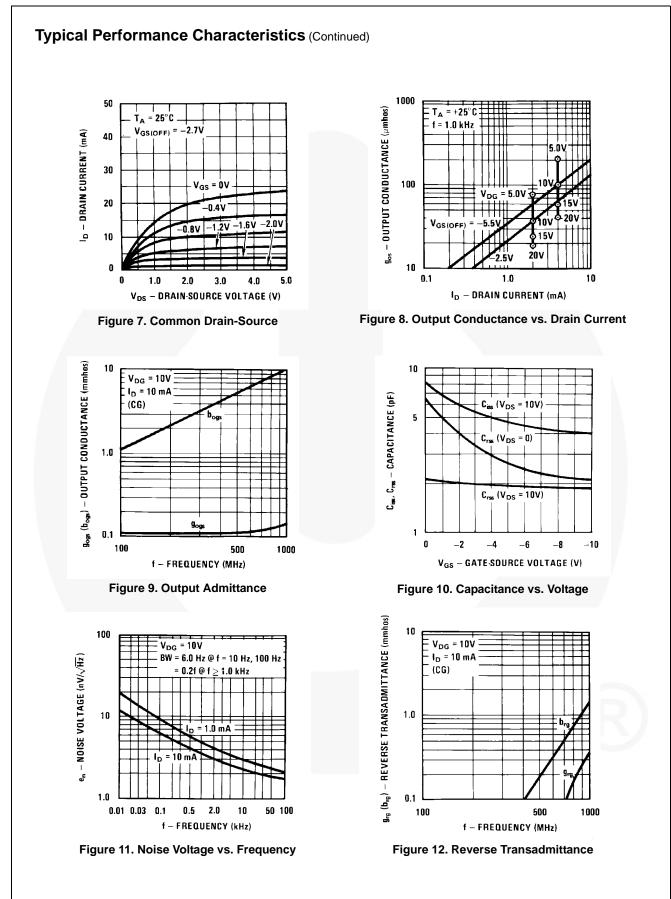
#### Note:

4. Pulse test: pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2.0%

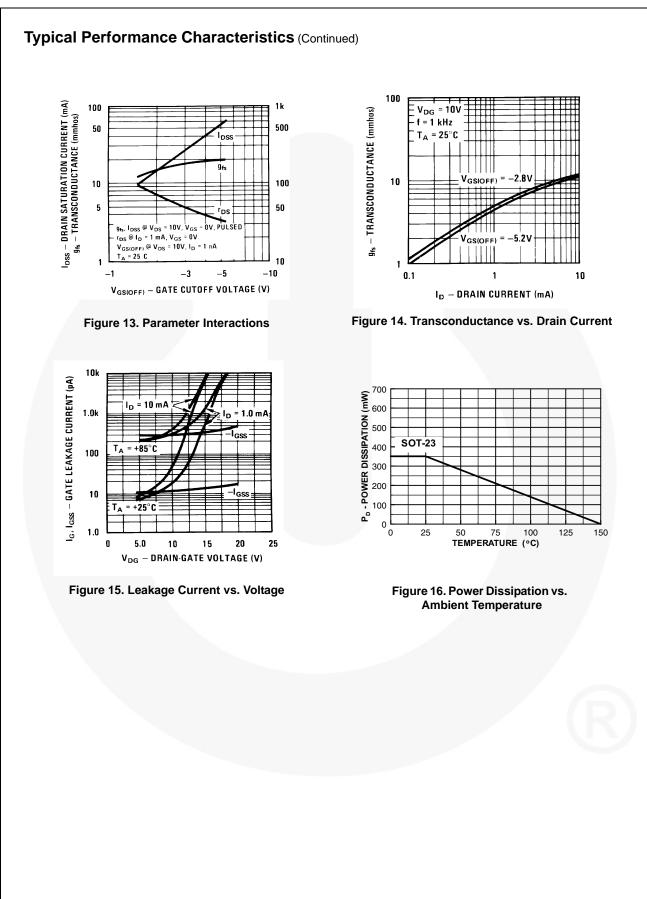


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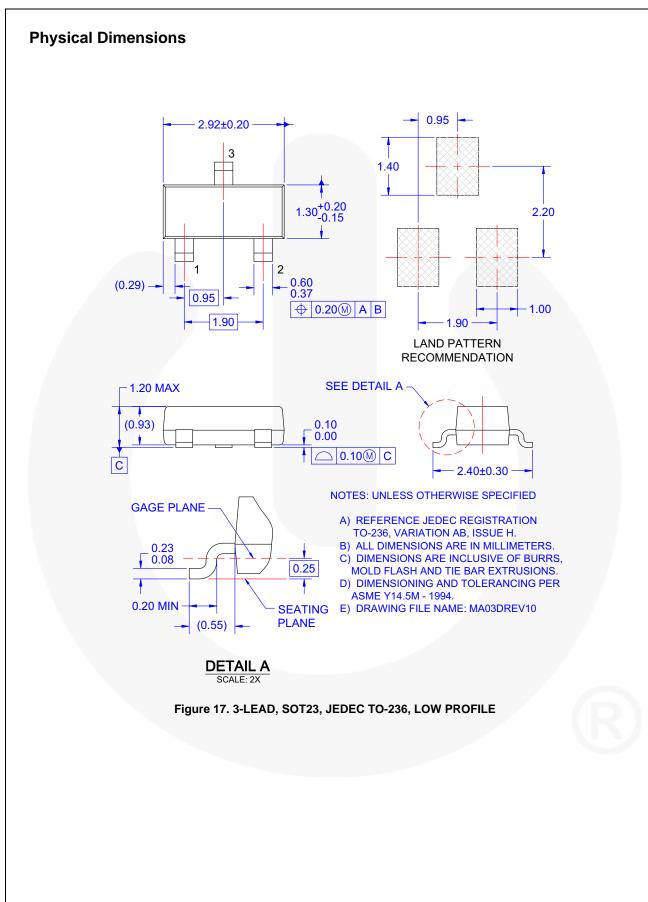




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